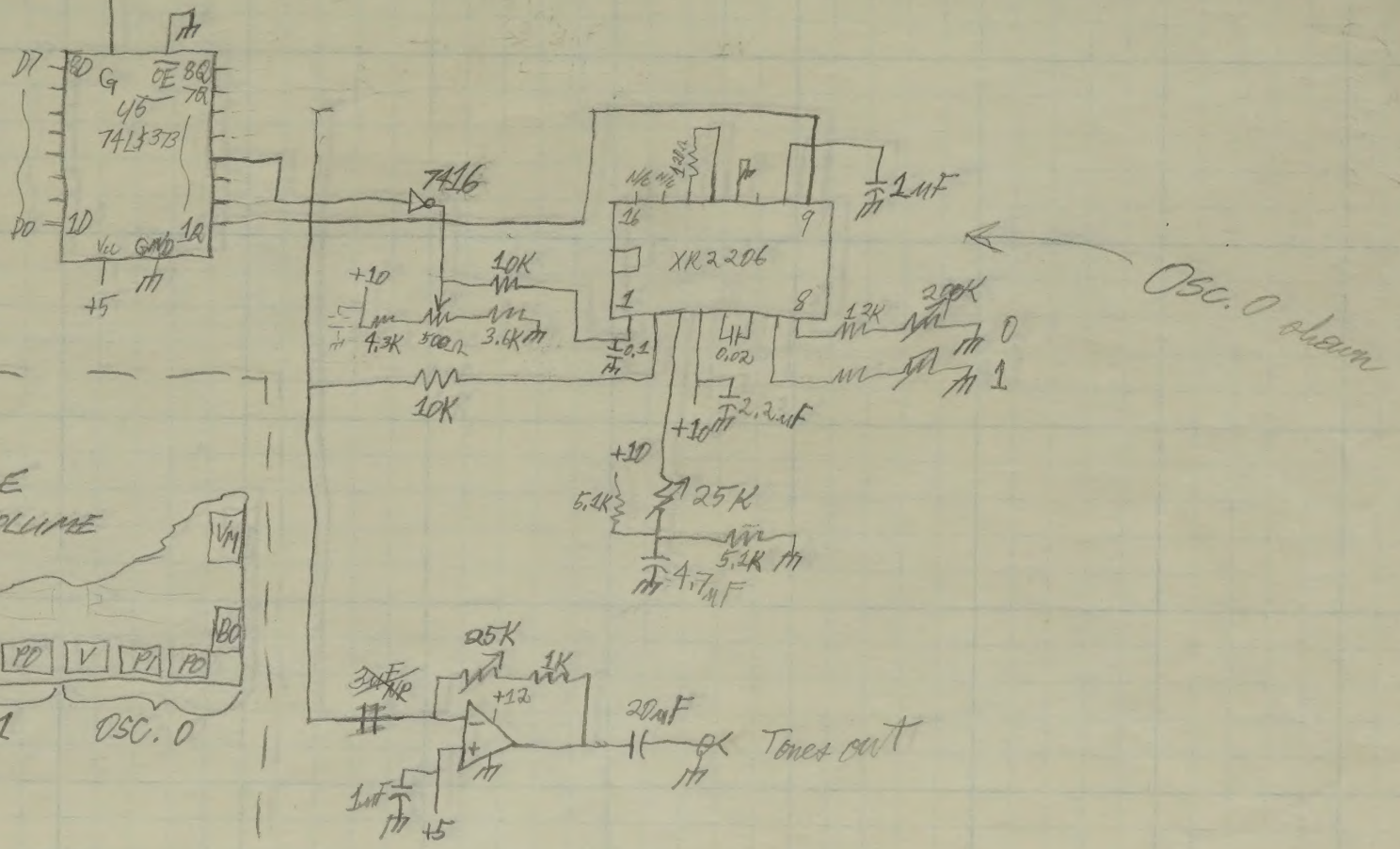
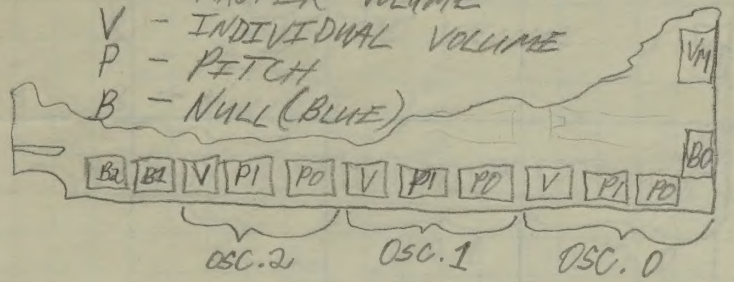
I/O Board
Pg. 2



CONTROL DETAIL.

VM - MASTER VOLUME
V - INDIVIDUAL VOLUME
P - PITCH
B - NULL (BLUE)



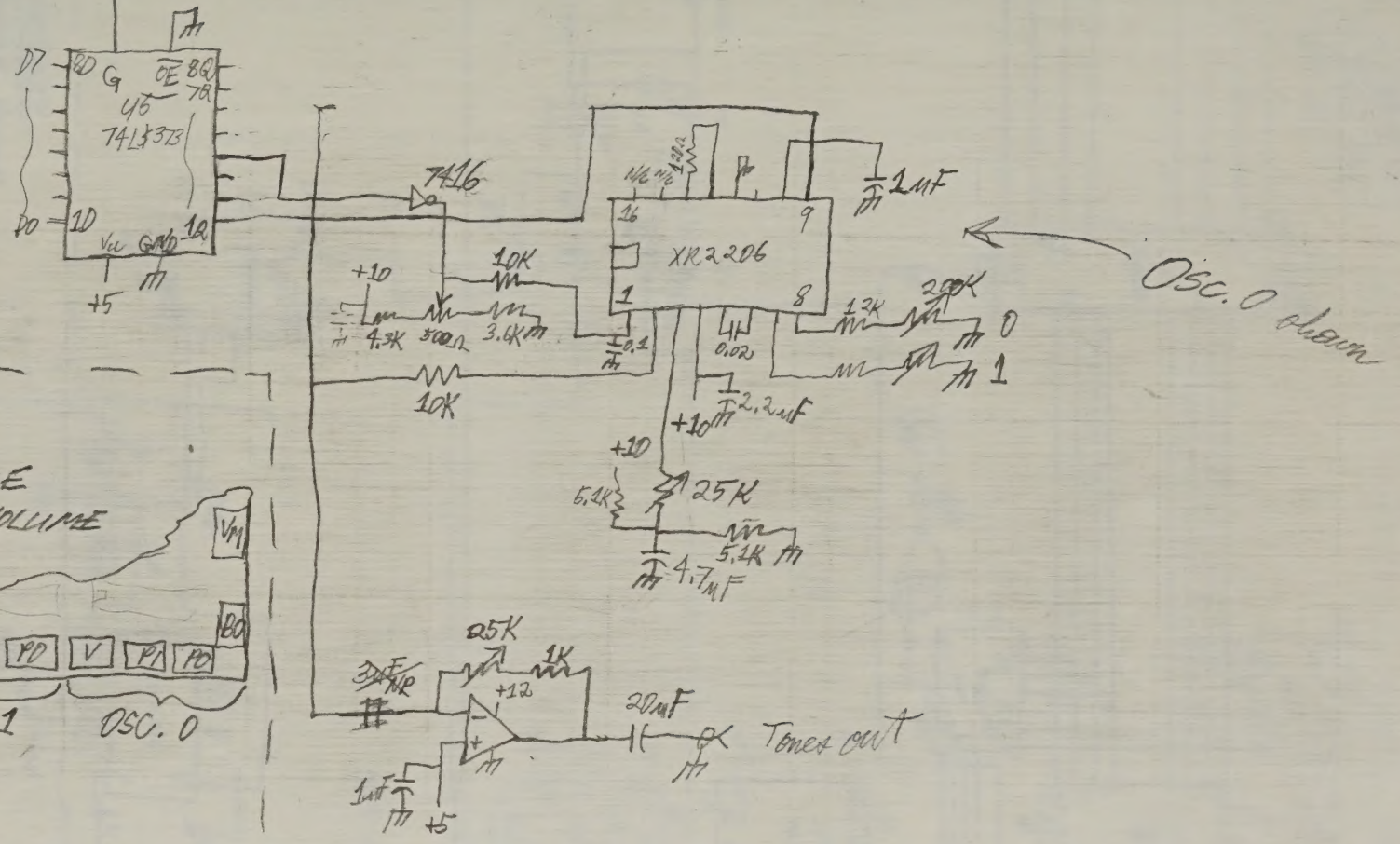
TELEMETRY BYTE:

BIT:	7 6	5 4 3	2 1 0
OSC:	X X	2 1 0	2 1 0
FTN:	X X	PITCH	ENABLE(1)

OSC 0 ⇒ HIGH ID, LOW ID, 2M, UHF, LOCAL
PORT 0, PORT 4, PORT 5

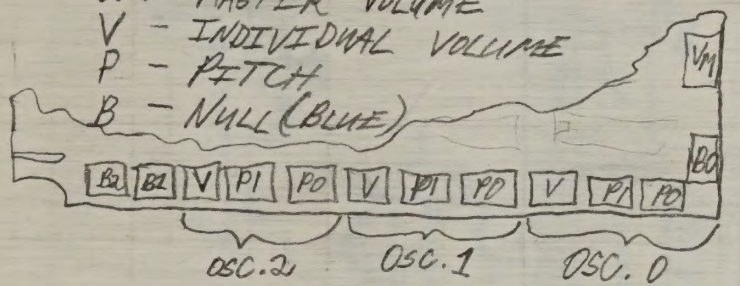
OSC 2 ⇒ QEV, GM, REVERSE PATCH RINGER
PORT 1, PORT 2

OSC 1 ⇒ PORT 3, "R", PATCH TIME OUT



CONTROL DETAIL.

VM - MASTER VOLUME
V - INDIVIDUAL VOLUME
P - PITCH
B - NULL (BLUE)



TELEMETRY BYTE:

BIT:	7 6	5 4 3	2 1 0
OSC:	X X	2 1 0	2 1 0
FTN:	X X	PITCH	ENABLE(1)

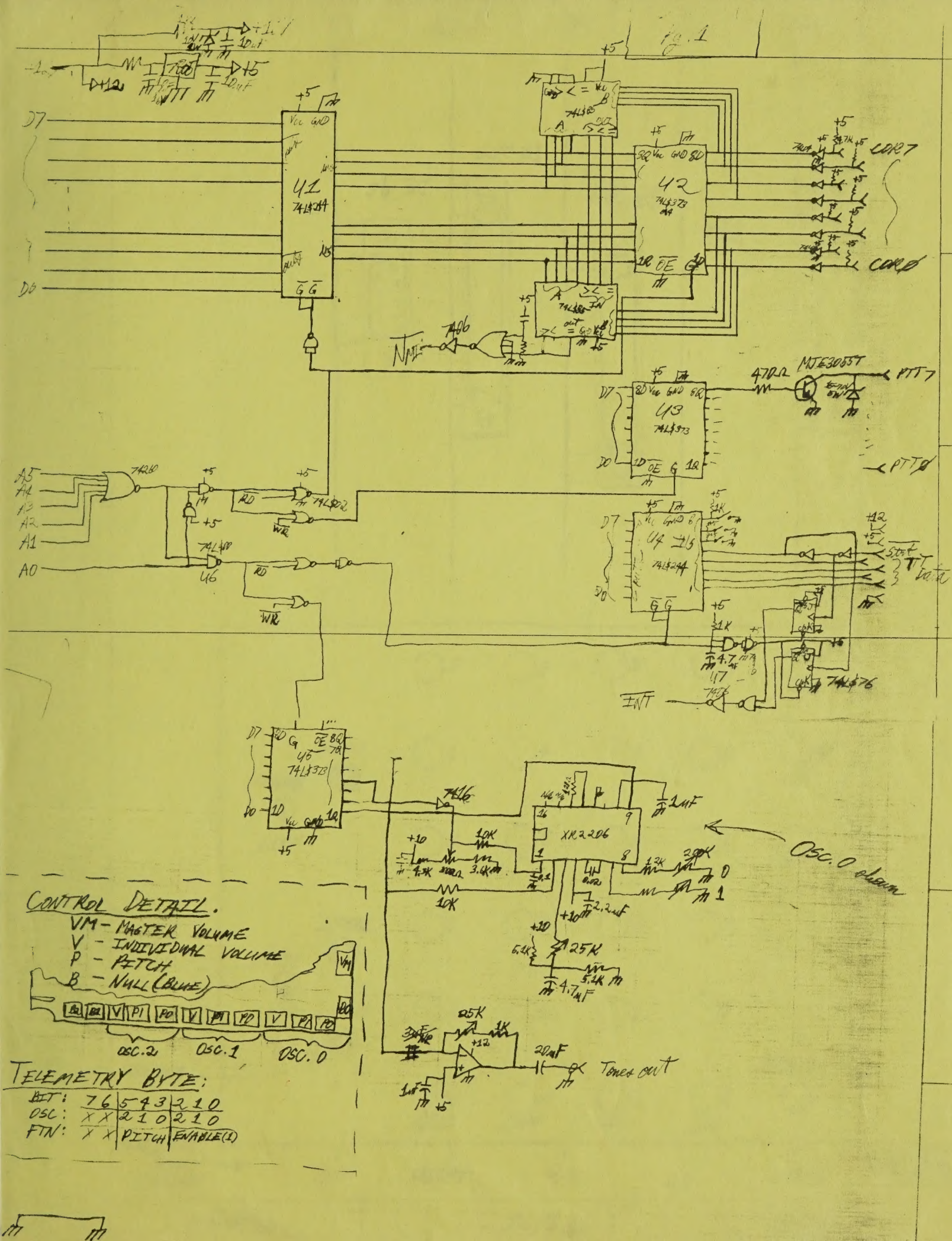
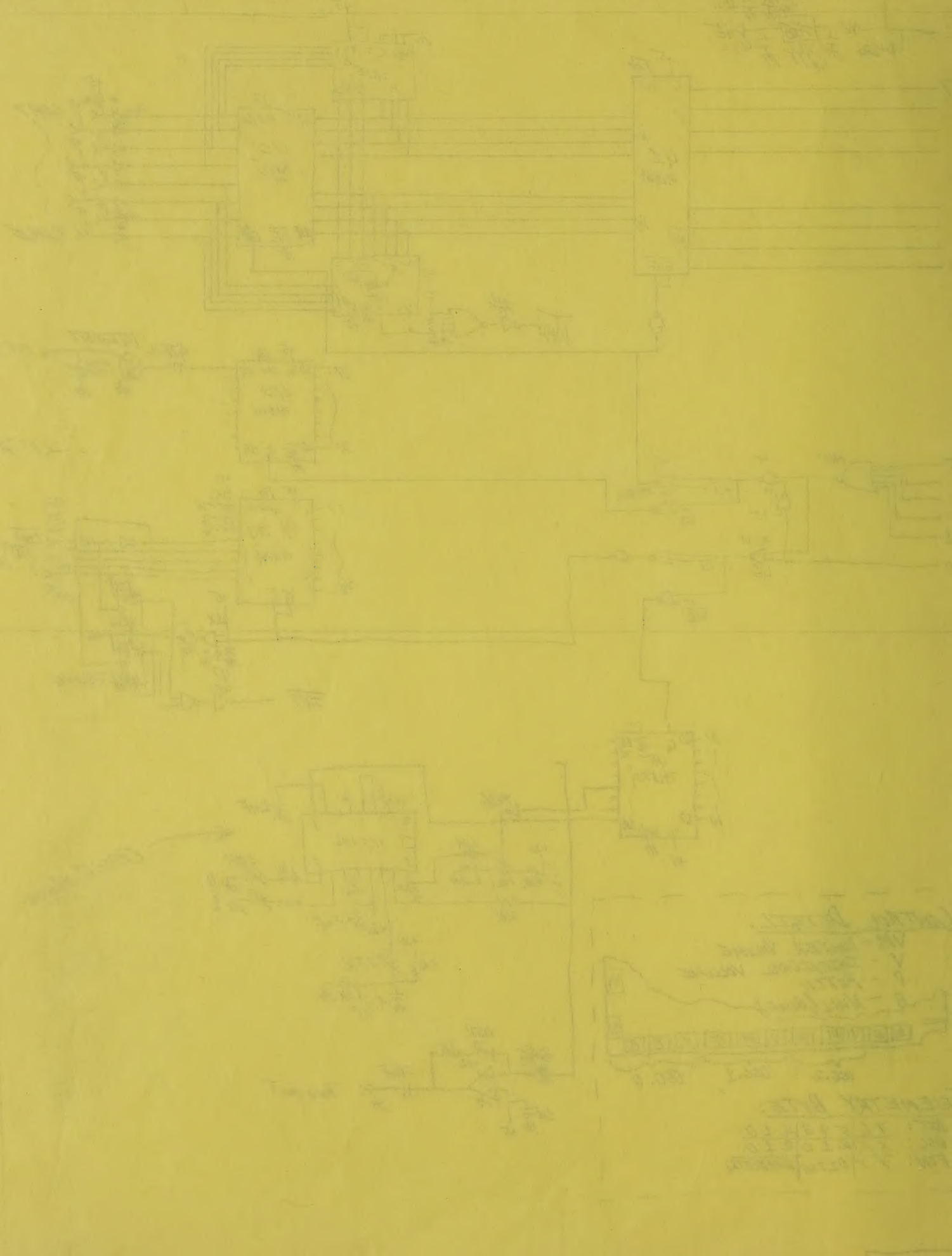
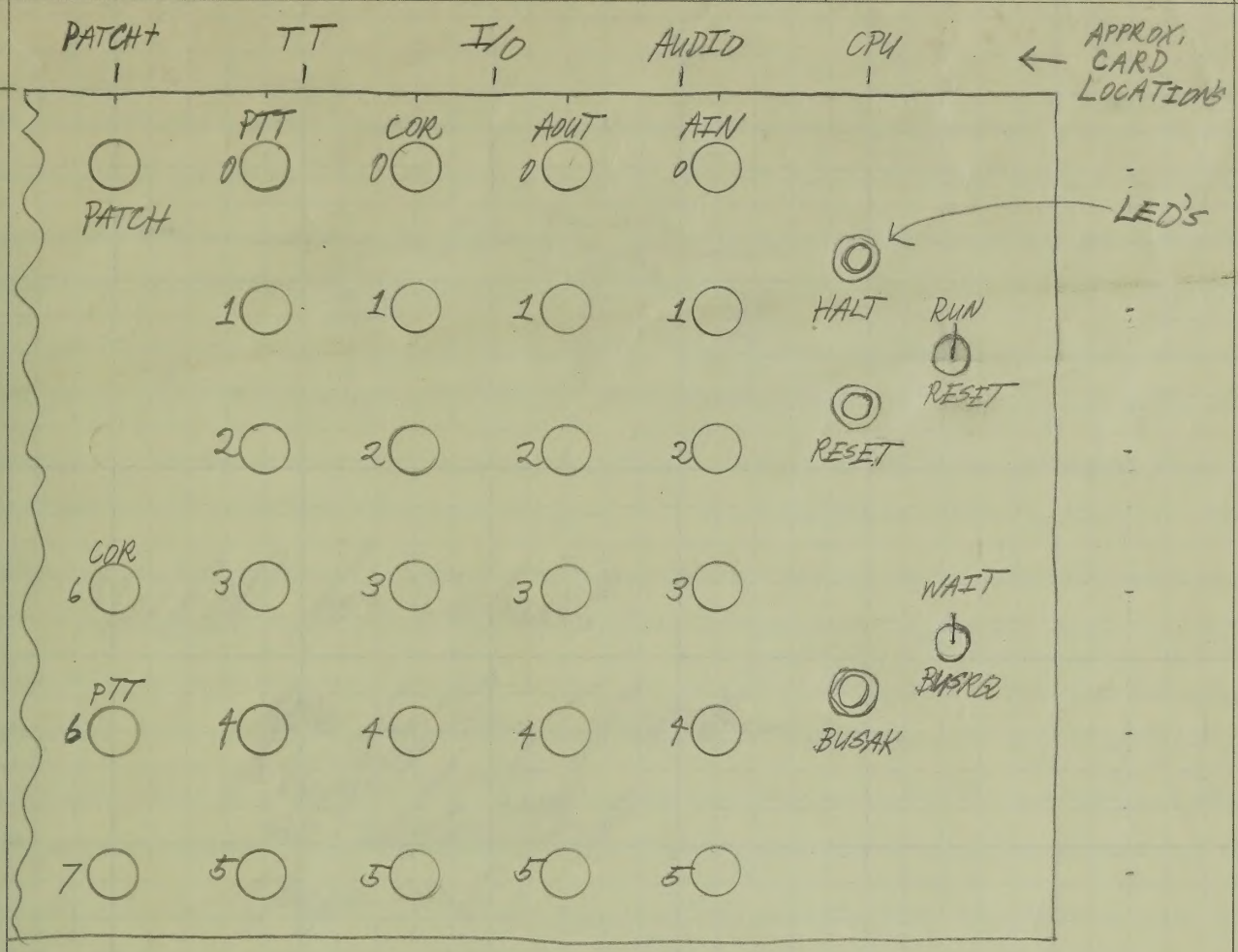


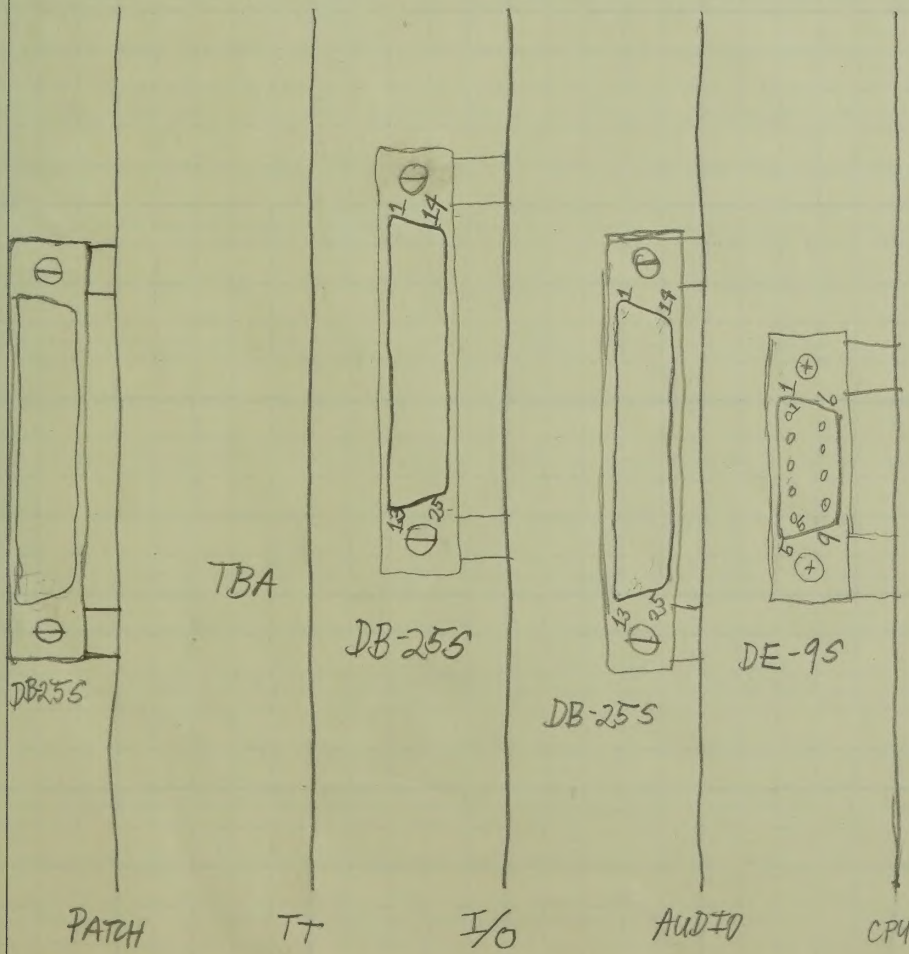
Fig. 1
U1
74LS244
U2
74LS373
U3
74LS373
U4
74LS244
U5
74LS373
U6
U7
74LS76
XR2206
MJE3055T
470Ω
PTT7
PTT0
CORE7
CORE0
NMI
INT
RD
WR
Osc. 0 down
Tone out
CONTROL DETAIL.
VM - MASTER VOLUME
V - INDIVIDUAL VOLUME
P - PITCH
B - NULL (BLUE)
OSC.2
OSC.1
OSC.0
TELEMETRY BYTE:
BIT: 7 6 5 4 3 2 1 0
OSC: X X 2 1 0 2 1 0
FTN: X X PITCH ENABLE(1)

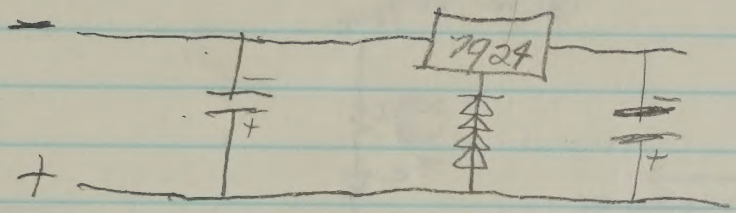


4 JAN, '84 | HP1676 | MAGNUM

FRONT PANEL





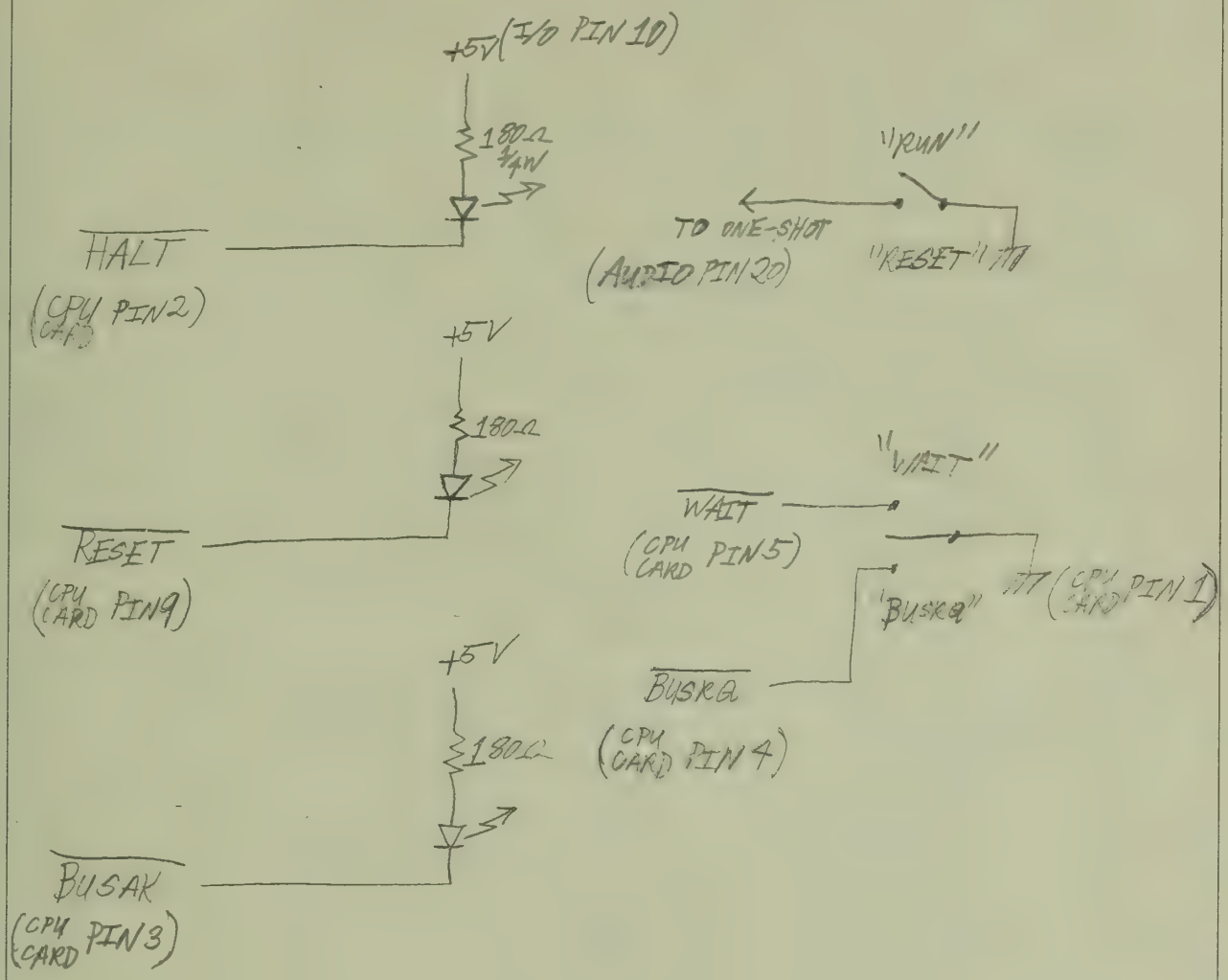


Needed for cabinet:

fuse holder / fused
Phone mod jack
25' telephone mod cord
(page card)

7 JAN, '84	HP1676	WW6DWH

FRONT PANEL DISPLAY



Updated 9-20-83

A_O0 - 2m TX	PTT0 - 2m TX
A_O1 - 220 link TX	PTT1 - 220 link TX
A_O2 - 6m TX	PTT2 - 6m TX
A_O3	PTT3 - 52525/49 6m
A_O4	PTT4
A_O5	PTT5
A_O6 - Patch	PTT6 -
A_O7 - Telemetry	PTT7 - Buffalo-up

A_I0 - 2m RX	COR0 - 2m RX
A_I1 - 220 RX	COR1 - 220 link RX
A_I2 - 6m RX	COR2 - 6m RX
A_I3 - 450 control	COR3 - 450 control
A_I4 -	COR4 -
A_I5	COR5 - (Ring Detect)
A_I6 - Patch	COR6 - Batt. power
A_I7 - Telemetry + Tenacity	COR7 - Interrupt osc. (≈18Hz)

I/O board

ADDR

00

8 active low inputs (

8 active low

01

8 bit inputs (4 or 5 for TT decoder,

Dual [illegible] : 350, 740 01

[illegible] : 450, 620 10

[illegible] : 450, 740 11

=DEK,

FUNCTION COMPLETE

TX OK

HP1

I6

Qty.	Description
2	74L(S)04
2	74L$85
1	7407
2	74L00
1	74L$02
1	74L$260
2	74L$244
3	74L$373
13	1K 1/4W or 1/8W
5	330Ω 1/4W 10%
8	68Ω 1/4W or 1/8W
1	40Ω 1W
3	120Ω 1/4W
4	3.6K 1/4W or 1/8W
1	2K 1/4W or 1/8W
6	5.1K 1/4W or 1/8W
4	10K 1/4W or 1/8W
6	12K 1/4W or 1/8W
1	13K 1/4W or 1/8W
3	0.02 uF
3	10 uF 16V Tant
4	4.7 uF 10V Tant

I/O Board Parts List, cont.

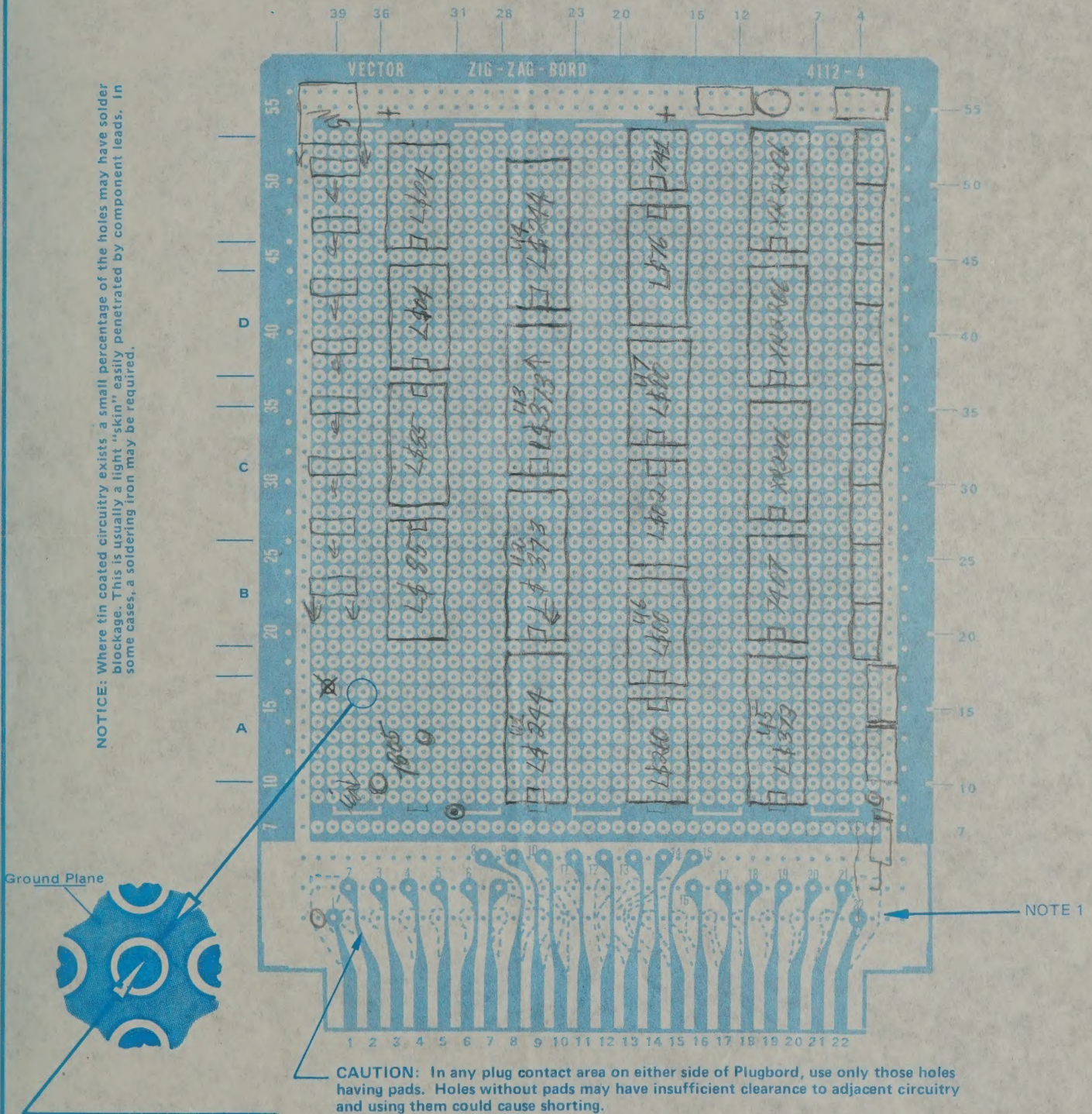
Quan	Description
6	2.2 uF 10V Tant
3	2.2 uF 12V Tant
1	33 uF 12V Tant or Elect
2	3 uF non polar 12V
8	TIP 110 (or 111, 112, 120, 121, 122)
8	18V 5W Zener
1	10V 1W Zener
7	14 DIP solder socket, Low prof.
5	16 " " " " "
5	20 " " " " "
3	XR 2206
1	LM 741
1	8 DIP solder socket, Low prof.
1	DB 25F
~~2~~	~~RCA Female~~
1	MC 7805 CK (TO-3)
1	15 Ω 2W
~~1~~	~~small TO3 Heat sink~~
1	4 pos DIP Sw

HP1676 I/O CARD

VECTOR ELECTRONIC COMPANY, INCORPORATED
12460 GLADSTONE AVENUE, SYLMAR, CALIF. 91342

LAYOUT PAPER FOR 4112-4 SERIES ZIG-ZAG PLUGBORD

COMPONENT SIDE



NOTICE: Where tin coated circuitry exists a small percentage of the holes may have solder blockage. This is usually a light "skin" easily penetrated by component leads. In some cases, a soldering iron may be required.

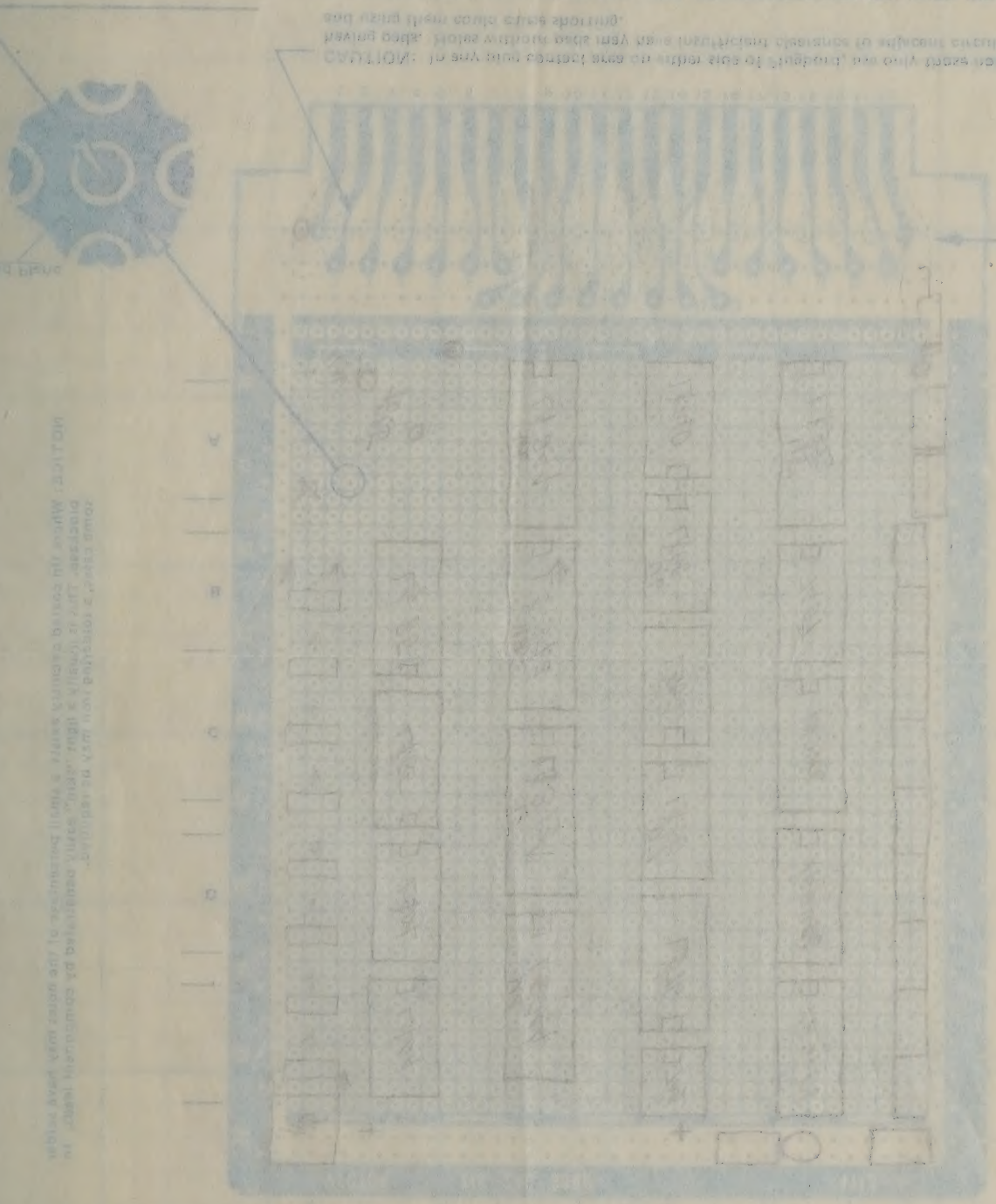
CAUTION: In any plug contact area on either side of Plugbord, use only those holes having pads. Holes without pads may have insufficient clearance to adjacent circuitry and using them could cause shorting.

2. IN ACCORDANCE WITH VECTOR'S CONSUMER PROTECTION POLICIES, WE SUGGEST YOU INSPECT THE BOARD BEFORE ASSEMBLY TO VERIFY ADEQUATE CLEARANCE WILL EXIST BETWEEN THE GROUND PLANE SURROUNDING THE HOLES AND ANY LEADS OR TERMINALS INSTALLED IN HOLES SO THAT SHORTING WILL NOT OCCUR, THIS BOARD IS INTENDED FOR USE IN NON-HOSTILE ENVIRONMENTS UP TO 200 VOLTS RMS OR 300 VOLTS DC.

1. OPEN PADS REPRESENT CONTACTS ON OPPOSITE SIDE OF BOARD.

NOTES:

VECTOR D.I.P. PLUGBORD
"P" PATTERN .042" DIA. x 0.1" SPACED HOLES
LA7-P2 LAYOUT PAPER



COMPONENT SIDE